

[0063] ABSTRACT

[0064] An active arc suppression circuit and systems and methods of use to suppress arcing in an electro-mechanical apparatus. The preferred circuit includes an electro-mechanical switch and a solid state shunt switch for temporarily shunting current around the electro-mechanical switch for a predetermined period of time. The preferred circuit also includes an electro-mechanical switch controller for delaying the activation of the electro-mechanical switch until after the predetermined period of time for shunting current through the solid state shunt switch has commenced. The preferred circuit may be used with power control equipment and systems, including in remotely controllable systems for telecommunications, computing, and other networks. In a particularly preferred embodiment, multiple such circuits may be disposed in a power controller housing to provide independent active arc suppression control of multiple power outputs also disposed in the power controller housing.